

Electronic Acknowledgement Receipt

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Title of Invention:	Method and structure for producing high performance linear algebra routines using composite blocking based on L1 cache size
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed	IDS.pdf	108218	no	3

Warnings:

Information:					
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2	Information Disclosure Statement (IDS) Filed	patent.pdf	80095	no	1
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3	NPL Documents	Reference.pdf	968200	no	16
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4	NPL Documents	Reference2.pdf	759522	no	10
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